IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:)
Yukihisa KINUGASA) Attn: Applications Branch
Serial No. Not Yet Assigned) Group Art Unit: Not Yet Assigned
Filed: October 30, 2003)
For: APPARATUS FOR CORRECTING OFFSET) ,
VOLTAGE) Date: October 30, 2003

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

In accordance with the duty of disclosure as set forth in 37 C.F.R. §1.56, Applicants hereby submit the following information in conformance with 37 C.F.R. §§ 1.97 and 1.98. Pursuant to 37 C.F.R. § 1.98, a copy of the documents cited on the PTO-1449 are enclosed and described in the specification of the above-captioned patent application.

It is requested that the accompanying PTO-1449 be considered and made of record in the above-identified application. To assist the Examiner, the documents are listed on the attached form PTO-1449. It is respectfully requested that an Examiner initialed copy of this form be returned to the undersigned.

The Commissioner is hereby authorized to charge any fees connected with this filing which may be required now, or credit any overpayment to Deposit Account No. 19-2380.

Respectfully submitted,

3y: __

Donald R. Studebaker Registration No. 32,815

NIXON PEABODY LLP 401 9th Street N.W. Suite 900 Washington, DC 20004-2128 (202) 585-8000

DRS/sas

Substitute for form 1449A/PTO	Complete if Known		
INFORMATION DISCLOSURE	Application Number	Not Yet Assigned	
STATEMENT BY APPLICANT	Filing Date	October 30, 2003	
, -	First Named Inventor	Yukihisa KINUGASA	
(use as many sheets as necessary)	Art Unit	Not Yet Assigned	
	Examiner Name	Not Yet Assigned	
Sheet I of I	Attorney Docket Number	031794-5	

	U.S. PATENT DOCUMENTS						
Examiner Initials	Cite No. ¹	U.S. Patent Document Number - Kind Code ² (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear		
	1	US-					
•		US-					
		US-					
		US-					
		US-					
		US-					
		US-					
		US-					
		US-					
		US-					

		FC	REIGN PATENT	DOCUMENTS	 		
Examiner Initials*	Cite No.	Foreign Patent Document Kind Code ³ Country Code ³ Number ⁴ (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Application of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T•	
		JP 11-88071 A	03/30/1999	T. Harada et al		W/Abstr	act
		JP 2001-44770 A	02/16/2001	K. Komatsu et al		W/Abstr	ract
	 	<u> </u>				'	
				<u> </u>	<u> </u>		
		OTHER PRIOR A	ART – NON PATENT	LITERATURE DOCUMENT	rs		
Examiner Initials	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.)., date, page(s), volume-issue number(s), publisher, city and/or country where published.					
		"Neuron-MOS Current Mirror Circuit and Its Application to Multi-Valued Logic" Jing SHEN et al. IEICE TRANS. INF. & SYST., VOL. E82-D, No. 5 May 1999 (pp 940-948)					

Examiner	Date	•	
Signature	Considered .		

^{*}EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

¹ Applicant's unique citation designation number (optional). ² See Kinds Codes of USPTO Patent Documents at www.uspto.gov or MPEP 901.04. ³ Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). ⁴ For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. ⁵ Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST.16 if possible. ⁶ Applicant is to place a check mark here if English language Translation is attached.

¹ Applicant's unique citation designation number (optional). ² Applicant is to place a check mark here if English language Translation is attached.